

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Kenichi ADACHI et al.

Application No.: 10/575,041

Filing Date: April 5, 2006

For: Processor For Controlling Performance In  
Accordance With A Chip Temperature,  
Information Processing Apparatus, And Method  
Of Controlling Same

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Confirmation No. 5951

Group Art Unit: Not yet assigned

Examiner: Not yet assigned

Attorney Docket No.: 545/72

**Mail Stop: PCT**

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTIFICATION OF MISSING REQUIREMENTS**

Sir:

In response to the Notice of Missing Requirements dated January 5, 2007, enclosed herewith is a translation of PCT Application No.: PCT/JP2005/006965, a Supplemental Preliminary Amendment, and a processing fee of \$130.00 under 37 CFR 1.492(i).

In the event there are any fees due and owing in connection with this matter, please charge same to our Deposit Account No. 11-0223.

Dated: February 22, 2007

Respectfully submitted,

By s/Matthew B. Dernier/  
Matthew B. Dernier  
Registration No.: 40,989  
KAPLAN GILMAN GIBSON & DERNIER LLP  
900 Route 9 North  
Woodbridge, New Jersey 07095  
Telephone (732) 634-7634  
*Attorneys for Applicant*